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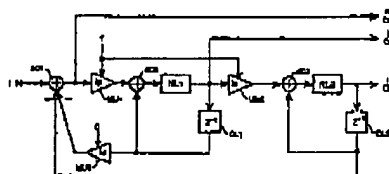
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(54) DIGITAL FILTERING MEANS

(57) Abstract:

PROBLEM TO BE SOLVED: To provide an action which is proper to an analog filter with a digital filter.

SOLUTION: A 1st non-linear circuit NL1 of a soft limiter characteristic is inserted into a 1st accumulator that consists of a 1st adder AD2 and a 1st delay circuit DL1 that performs one sampling cycle delay. A 2nd non-linear circuit NL2 is inserted into a 2nd accumulator which consists of a 2nd adder AD3 and a 2nd delay circuit DL2 that performs one sampling cycle delay. Even though the adders AD2 and AD3 are undergone overflow limit by the circuits NL1 and NL2, a moderate distortion is given to an inputted signal without being rapidly clipped.



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